

| L Number | Hits    | Search Text   | DB  | Time stamp       |
|----------|---------|---|---|------------------|
| 1        | 1089654 | CPU or (controller\$1 or microcontroller\$1)<br><i>or Microprocesas</i>   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:47 |
| 2        | 11584   | (CPU or (controller\$1 or microcontroller\$1)) and (initializ\$4 same ((complet\$3 or finish\$3) or (success\$3 or fail\$3)))   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:48 |
| 3        | 1390    | (CPU or (controller\$1 or microcontroller\$1)) and (initializ\$4 same (((complet\$3 or finish\$3) or (success\$3 or fail\$3)) with signal))   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 12:33 |
| 4        | 670     | ((CPU or (controller\$1 or microcontroller\$1)) and (initializ\$4 same (((complet\$3 or finish\$3) or (success\$3 or fail\$3)) with signal)) ) and ((CPU or (controller\$1 or microcontroller\$1)) same (enabl\$3 or permit\$4))  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:49 |
| 5        | 299     | ((CPU or (controller\$1 or microcontroller\$1)) and (initializ\$4 same (((complet\$3 or finish\$3) or (success\$3 or fail\$3)) with signal)) ) and ((CPU or (controller\$1 or microcontroller\$1)) same (enabl\$3 or permit\$4))) and print\$4                            | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:00 |
| 6        | 13      | ((CPU or (controller\$1 or microcontroller\$1)) and (initializ\$4 same (((complet\$3 or finish\$3) or (success\$3 or fail\$3)) with signal)) ) and ((CPU or (controller\$1 or microcontroller\$1)) same (enabl\$3 or permit\$4))) and print\$4 and ink-jet                | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:00 |
| 7        | 0       | ((CPU or (controller\$1 or microcontroller\$1)) and (initializ\$4 same (((complet\$3 or finish\$3) or (success\$3 or fail\$3)) with signal)) ) and ((CPU or (controller\$1 or microcontroller\$1)) same (enabl\$3 or permit\$4))) and "power on"                          | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:11 |
| 8        | 0       | CPU or (controller\$1 or microcontroller\$1)) and ((initializ\$4 same ((complet\$3 or finish\$3) or (success\$3 or fail\$3))) and "power on")   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:11 |
| 9        | 111     | ((CPU or (controller\$1 or microcontroller\$1)) and (initializ\$4 same (((complet\$3 or finish\$3) or (success\$3 or fail\$3)) with signal)) ) and ((CPU or (controller\$1 or microcontroller\$1)) same (enabl\$3 or permit\$4))) and "power-on"                          | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:12 |
| 10       | 52      | ((CPU or (controller\$1 or microcontroller\$1)) and (initializ\$4 same (((complet\$3 or finish\$3) or (success\$3 or fail\$3)) with signal)) ) and ((CPU or (controller\$1 or microcontroller\$1)) same (enabl\$3 or permit\$4))) and "power-on" and print\$4             | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:47 |
| 11       | 0       | ((CPU or (controller\$1 or microcontroller\$1)) and (initializ\$4 same (((complet\$3 or finish\$3) or (success\$3 or fail\$3)) with signal)) ) and ((CPU or (controller\$1 or microcontroller\$1)) same (enabl\$3 or permit\$4))) and "power-on" and print\$4 and ink-jet | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:47 |
| 12       | 3141    | ink-jet and (CPU or (controller\$1 or microcontroller\$1))<br>..  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:48 |
| 13       | 80      | (ink-jet and (CPU or (controller\$1 or microcontroller\$1))) and (initializ\$4 same ((complet\$3 or finish\$3) or (success\$3 or fail\$3)))   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:49 |

inkjet

|    |    |  |   |                  |
|----|----|--|---|------------------|
| 14 | 33 | ((ink-jet and (CPU or (controller\$1 or microcontroller\$1))) and (initializ\$4 same ((complet\$3 or finish\$3) or (success\$3 or fail\$3))) ) and ((CPU or (controller\$1 or microcontroller\$1)) same (enabl\$3 or permit\$4)) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/06/04 13:50 |
|----|----|--|---|------------------|

↓  
microprocessor